



# Performance Evaluation of Co-Design of Discrete Event Networked Controlled DC Motor System

Ankur Jain<sup>1\*</sup>, B. K. Roy<sup>2</sup>

<sup>1,2</sup>National Institute of Technology, Silchar, India

\*Corresponding Author Email: <sup>1</sup>[ankurjainjob@gmail.com](mailto:ankurjainjob@gmail.com)

## Abstract

In this paper, we have designed a control technique for a networked DC motor in the presence of networked delay, packet loss, and jitter. We have used the predicted states for the controller design to achieve the transient and steady-state objectives. A networked compensator is designed to overcome the network constraints. The network link is modelled using the queue server mechanism which can assimilate a lot of features of the network. The proposed technique can also be applied to various other applications. The analysis of the networked control system is done in frequency and time domains. The simulation results are presented to test the performance of the proposed control technique.

**Keywords:** Controller design; Internet of Things (IoT); Networked control system (NCS); DC motor speed control.

## 1. Introduction

The networked control system (NCS) is an interdisciplinary subject which consists of control engineering, communication engineering [Error! Reference source not found.], [Error! Reference source not found.]. It can be seen as a distributed system [Error! Reference source not found.] which composed of computation in cyber-space, sensing in physical space and communication functionality to link cyber-space with physical space [Error! Reference source not found., Error! Reference source not found.]. Hence, it can also be called as one type of cyber-physical system (CPS) [Error! Reference source not found.]. The very fast growth in VLSI and ubiquitous computing [Error! Reference source not found.], cloud computing [Error! Reference source not found.], big data analysis [Error! Reference source not found.], has been shifting the control engineering into a new paradigm and leads to significant number of research publications [Error! Reference source not found.].

In NCS, control loop (feedback, forward, or both) is closed by a network [Error! Reference source not found., Error! Reference source not found.]. The introduction of a network gives some advantages like ease of maintenance, low power consumption in signal transmission, better resource sharing, better data handling, low-cost control, less wiring, etc. At the same time, a network introduces delay (D), dropout (DP), packet disorder (DO), limited bandwidth (LBW), less computational

power at plant side and high computational power on cyber side, etc. Various network constraints are reported in the literature and are listed Table 1.

**Table 1:** The literature survey on network constraints

Time delay	[Error! Reference source not found., Error! Reference source not found., Error! Reference source not found., Error! Reference source not found., Error! Reference source not found.]
Packet dropout	[Error! Reference source not found., Error! Reference source not found., Error! Reference source not found., Error! Reference source not found., Error! Reference source not found., Error! Reference source not found.]
Packet disorder	[Error! Reference source not found., Error! Reference source not found.]
Jitter	[Error! Reference source not found., Error! Reference source not found.]
Limited bandwidth	[Error! Reference source not found., Error! Reference source not found.]
Scheduling	[Error! Reference source not found., Error! Reference source not found., Error! Reference source not found.]
Security	[Error! Reference source not found.]

Among all the mentioned constraints, delay, dropout and jitter have the major influences on the performance. These constraints may even cause instability. These constraints are modelled in various ways in the literature such as Markov chain [Error! Reference source not found.], transportation delay [Error! Reference source not found.], virtual switch [Error!



**Fig. 1:** Electrical equivalent circuit of the DC motor

By torque v/s armature current characteristics of the DC motor, we know that torque generated is directly proportional to the armature current 'i'.

$$T = K_t i \tag{1}$$

where  $K_t$  is a torque constant. The back emf,  $E_b$  is proportional to the angular velocity of the motor shaft.

$$E_b = K_e \dot{\theta} \tag{2}$$

where  $K_e$  is a back emf constant. In SI units, the motor torque and back emf constants are equal, that is,  $K_t = K_e$  so from there onwards we can use  $K_c$  for the sake of notation simplicity. by Newton's 2nd law and Kirchoff voltage law, we can derive following equations from Fig. 1,

$$J\ddot{\theta} + b\dot{\theta} = K_c i \tag{3}$$

$$L \frac{di}{dt} + Ri = V - K_c \dot{\theta} \tag{4}$$

Here we assumed that magnetic field is constant. Motor shaft and rotor are rigid in nature. Let maximum and the minimum voltage applied to DC motor are  $V_{max}$  and  $V_{min}$ .

For the purpose of analysis and controller design, we can transform Eq. 3 and Eq. 4 into a state-space form which is shown in Eq. 5 and Eq. 6, here we have chosen the rotational speed  $\dot{\theta}$  and electric current  $i$  as the state variables. In this problem, armature voltage and rotational speed will be input and output respectively.

$$\frac{d}{dt} \begin{bmatrix} \dot{\theta} \\ i \end{bmatrix} = \underbrace{\begin{bmatrix} -\frac{b}{J} & \frac{K_c}{J} \\ -\frac{K_c}{L} & -\frac{R}{L} \end{bmatrix}}_A \begin{bmatrix} \dot{\theta} \\ i \end{bmatrix} + \underbrace{\begin{bmatrix} 0 \\ \frac{1}{L} \end{bmatrix}}_B V \tag{5}$$

$$y = \underbrace{\begin{bmatrix} 1 & 0 \end{bmatrix}}_C \begin{bmatrix} \dot{\theta} \\ i \end{bmatrix} \tag{6}$$

Above equations describe the model of single DC motor. This plant could be written in the following manner:

$$(7)$$

$$u(t) = Kx(t_k), \forall t \in [t_k, t_{k+1}), \forall k \in \mathbb{Z} \tag{8}$$

Here, the plant is modelled as linear time-invariant system and controller is modelled as linear state feedback controller, where  $K$  is controller gain. Let's suppose sampler is being activated at the time instances  $t_k = \{1, 2, 3, \dots\}$ , and in between time instances sample value is piece-wise constant. Whenever the controller is activated, it takes the latest packet available at the buffer and calculates control input. The above system Eq. 7 & 8 could be written in discrete form as

$$x_{k+1} = A_d(h)x_k, \forall k \in \mathbb{Z}$$

$$A_d = e^{Ah} + \int_0^h e^{As} ds BK$$

$$(9)$$

where,

$x_k = x(t_k)$  and  $h$  is the constant sampling time. In case of time-varying sampling, the analysis of the above system is very complex. The networked control system operates in presence of communication delay which is shown in Fig. 2. We can modify the system Eq. 7 & 8 as

$$u(t) = Kx(t_k) = Kx(t - \tau(t)) \tag{10}$$

where, delay  $\tau(t) = t - t_k, \forall t \in [t_k, t_{k+1})$ , is a piecewise constant. So a time-varying system could be written as below,

$$\dot{x}(t) = Ax(t) + BKx(t - \tau(t)), \forall t \geq 0 \tag{11}$$

### 3. Modelling of the Network

Here, we have modeled the network with the help of queuing theory. It is an arrangement of entities in a sequential manner. For this particular paper, we are considering a packet as an entity. Here packet is used for information transformation from the sensor to the controller (samples) and from the controller to an actuator (controller input values). The server is a physical or a logical node which can provide a particular type of computation service.

Here we would like to study effects of the network on the performance of the control system. The network characteristics like delay, packet loss, network Bandwidth, scheduling policy, etc. could be modelled by a logical queue and server arrangement. In this paper we have modelled networked delay as service time of the server, packet dropout could be modelled by a switch with a terminator block. Terminator block shows the lost packet. Scheduling policy could be created by the priority queue. Higher priority could be given to concerned packet from a particular plant or from a particular time.

Here we have assumed round-robin scheduling policy. Only one packet could be transmitted at a time. The network has a minimum ( $T_{min}$ ) and maximum ( $T_{max}$ ) transmission time. The network has maximum packet dropout probability ( $P_{drop}$ ). Here, our objectives are to design controller gain  $K$  while ensuring stability under the networked constraints. Meet performance objective (maximum overshoot should be less than 3%, settling time should be less than 2seconds, steady state error should be less than 2%).

### 4. Proposed Approach

Here we are proposing a discrete event full state feedback networked control system architecture which is shown in Fig. 2.

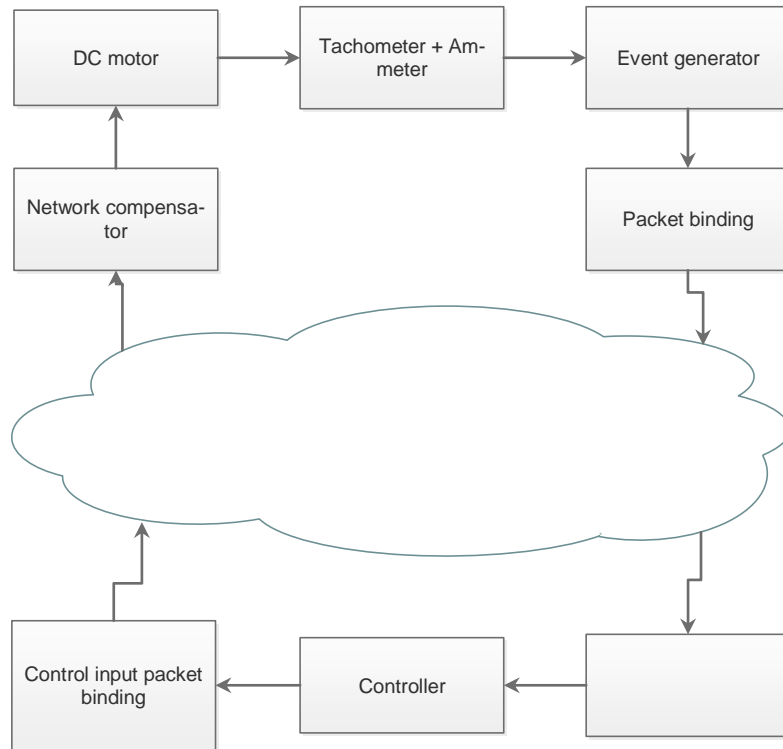


Fig. 2: Networked control system architecture

4.1 Design of Discrete Event Generation

A Continuous event (packet) generation [Error! Reference source not found.] could block network which results in packet loss or information delay. Here our objective is to reduced packet generation as much as possible until control performance is compromised. Here we are going to present a state differential triggering method for event generation. Here we can observe the local state in continuous, periodic or predicted instants, then compare the threshold condition, if the sample violates the threshold condition then the particular sample is encapsulated into a packet and sent to the controller otherwise the packet

is dropped locally. Here, Event is defined when a current sample of observing state is more than a certain threshold value of the previously sent packet. Mathematically it could define as

$$x(k_i - 1) - x(t_i) > \delta \tag{12}$$

where,  $t_i$ = time instant at which state is being observed,  $k_i$ = time instants at which packet is being sent,  $x(k_i)$  = value of the sample at  $k_i$  instant,  $x(t_i)$  = value of the sample at  $t_i$  instant,  $\delta$  = value of the threshold.

4.2 Packet Binding

We can send measurements and control sequence in one packet. Here we considered an entity as a packet which is used for the information exchange among nodes. Here, plant and controller are considered as different nodes which are connected by a packet-based network. Presently we have two existing packet type which is universal datagram protocol (UDP) and transmission control protocol (TCP). A packet consists of a header (overhead) and payload (Data).

4.3 Controller Design

Prediction of states: Let’s suppose there is a bounded maximum delay  $\tau$  for their physical significance. We can express this delay in multiple of sampling time. Assume that  $x(k_i)$  and  $u(k_i)$  are

the current sampled states and current control input respectively. Then from Eq. 7, we can derive that,

$$\begin{aligned} x(k_i + 1 | k_i) &= px(k_i) + qu(k_i) \\ x(k_i + 2 | k_i) &= p^2x(k_i) + pqu(k_i) \\ x(k_i + \tau | k_i) &= p^\tau x(k_i) + p^{\tau-1}qu(k_i) + p^{\tau-2}qu(k_i) \dots + qu(k_i) \end{aligned}$$

The above equations could be represented as an augmented model below

$$X = Px(k_i) + Qu(k_i) \tag{13}$$

where,

$$P = \begin{bmatrix} p \\ p^2 \\ \cdot \\ \cdot \\ p^\tau \end{bmatrix} \quad Q = \begin{bmatrix} q & 0 & 0 & \cdot & \cdot & 0 \\ pq & q & 0 & \cdot & \cdot & 0 \\ \cdot & \cdot & \cdot & \cdot & \cdot & \cdot \\ \cdot & \cdot & \cdot & \cdot & \cdot & \cdot \\ p^{\tau-1}q & p^{\tau-2}q & p^{\tau-3}q & \cdot & \cdot & q \end{bmatrix}$$

let’s suppose we would like to regularize our output at  $Ref(k_i)$ , So here we have the objective to design a controller which bring predicted output near to the set point as soon as possible. Assume that a reference data vector  $Ref(k_i)$  which have the set point information,

$$Ref(k_i)^T = \underbrace{[1 \ 1 \ \cdot \ \cdot \ 1]}_\tau s(k_i) \tag{14}$$

here, we can achieve our control objective by minimizing a cost function J,

$$J = (Ref(k_i) - X)^T (Ref(k_i) - X) + U^T \bar{R}U \tag{15}$$

we know then, for minimum J

$$\frac{\partial J}{\partial U} = 0$$

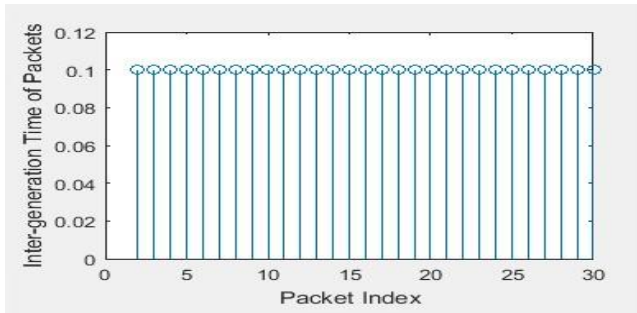
Then,

$$U = (Q^T Q + \bar{R})^{-1} Q^T (Ref(k_i) - Px(k_i)) \tag{16}$$

#### 4.4 Design of Network Effects Compensator

Introduction of network introduces random network delay which should be taken care to improve the performance of control design. We know that in a network transmission we can pack multiple data in a single packet. So, with the controller design in the previous section, we will predict multiple control input depend on the delay bound, and send to the network compensator. Network compensator chooses right control input for the real-time delay and sends the actuator. Mathematically it should be written as

$$u_i = c(\tau) * U \quad (17)$$

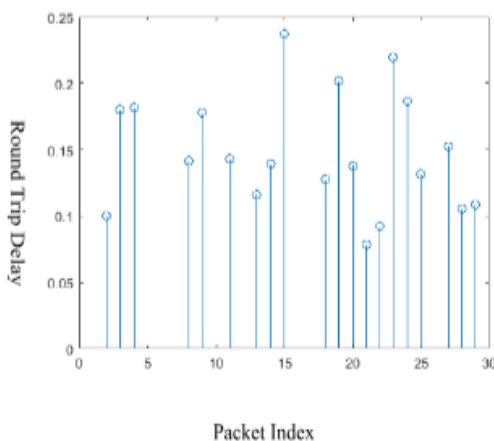


#### 5. Simulation Results

For verification of the compensation techniques and the controller performance, we have used armature controlled DC motor as a plant. Physical parameters like Moment of inertia (J), Viscous friction constant (b), force constant (K<sub>t</sub>), torque constant (K<sub>e</sub>), motor resistance (R), inductance (L) are taken as 0.01, 0.1 N.m.s, 0.01, 0.01, 1 Ohm, 0.5 respectively.

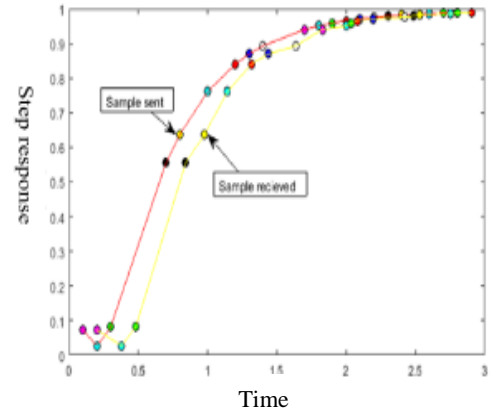
**Fig. 3:** Intergeneration time for checking states

Here to avoid complex continuous measurement and for saving energy we have used periodic event monitoring scheme which can be seen in Fig. 3, periodic check of state for event recognition is done with 0.1s intergeneration time(IGT).



**Fig. 4:** Random round trip delay for each packet

At each inter-generation time we check the threshold violation, if it occurs then the particular sample value is packed and sent to the controller, else it is discarded locally. In Fig. 4, random network delay is seen during transmission. Step response of DC motor for speed regulation could be seen in Fig. 5, all the transient and steady-state performance, we have decided is being full-filled by the proposed design.



**Fig. 5:** Step response of Speed control of DC motor

#### 6. Conclusion

Here, we have solved speed regulation Problem under random networked delay. In this paper, we have modelled NCS as queue server model which could easily capture the effects of network delay and packet dropout and jitter. Here we have considered round trip delay which is a summation of delay occurred in feedback channel, forward channel, computation delay, and delay due to packet loss. A predictive control scheme is used to generate future control input according to delay bound. A network effects compensator is designed. A simulation has been performed to show the suitability of the method. A controller is designed which is capable to meet the desired performance under networked scenario.

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