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Research Paper

Mat lab/simulink based fault analysis of pv grid with intelligent fuzzy logic control mppt

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Abstract

This paper mainly presents the fault analysis of Photovoltaic (PV) grid power system. The fuzzy logic controller (FLC) based intelligent maximum power point tracking (MPPT) algorithm has been employed in this work. Moreover, the hysteresis controller has been implemented for inverter control. Simulation results based on MATLAB/SIMULINK justify the effectiveness of the proposed PV power system under different fault operating conditions.

Keyword: *FLC; Hysteresis Controller; MATLAB; PV*

1. Introduction

Because of continuous depletion of fossil fuels and energy security challenges, renewable energy sources are playing important role for production of electrical power. Photovoltaic (PV), wind energy, fuel cell, tidal energy, geothermal energy etc are the main renewable energy concern [1]-[3].Compared to other renewable sources, the PV system is the most acceptable technology for electricity generation. As it has no moving parts and produce green and pollution free environment. However, because of high installation cost and low tracked efficiency, the maximum power point tracking (MPPT) technology has been added with PV power system [4]-[6]. Various MPPT algorithms have been used to improve the conversion efficiency of the PV module [7]. In view of this work fuzzy logic controller (FLC) based MPPT controller has been used to track the optimal power from PV modules [8]. Also, hysteresis controller for inverter current control strategy has been also employed [9]-[10]. The different faults L-G, L-L, LLL, LLG, and LLLG have been realized on grid side using MATLAB/SIMULINK environment.

1.1. Proposed structure of PV grid

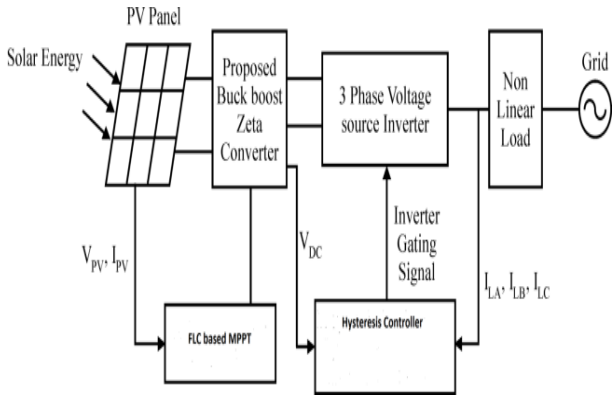


Fig. 1: Block Diagram of Proposed PV Power Circuit Based Utility Grid.

Fig 1.1 describes the structure of proposed PV grid with MPPT and inverter control. It comprises PV panel, buck-boost converter, inverter fed to the electric grid. The FLC based MPPT control is employed for buck boost converter. Hysteresis controller is also used to make sinusoidal inverter current for unity operated power factor.

1.2. Mathematical modeling of proposed zeta converter

State space model based zeta converter conducting in continuous mode is presented in this regard. Since zeta converter consists of two capacitors and two inductors and able to work in step down / up mode is called fourth order chopper which is depicted by Fig 1.2. With regard to the energy it acts as a buck converter, on the other hand with regard to the output it acts as a boost converter. The power circuit consists of capacitor and inductors as major elements which are employed for amplification and reduction of level of voltage. The complete operation is based on without polarity inversion and it acts as MPPT trackers which provide zero voltage ripple. Comparing to other dc-dc converters, it provides better voltage reg-

ulation, high tracked efficiency and reduction of stress level voltage. The main objective of maximum power point method is providing optimal PV power extraction. For maximizing the efficiency of buck-boost converter, the converters can be employed in continual conduction state. The supply voltage is same as system.
Output $V_S = V_{CS}$, in the purpose of dynamicity.
R_S and R_L represent the internal resistance of source and load respectvely. The following assumptions are made for analysis:

a) The converter operates in continuous inductor current mode.
b) Switching devices should be real.
c) DC voltage line frequency ripple is negligible

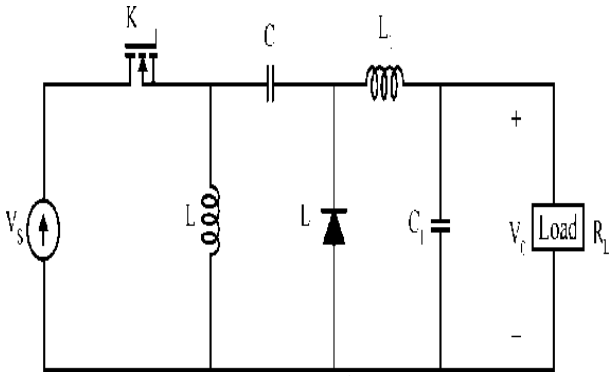


Fig. 2: Circuit Diagram of Zeta Converter.

Zeta converter is operating in two modes. In first, mode switch is opened and diode gets forward biased. The L_1 energy stored can be transferred to R_L. Also, when switch is closed, the diode gets reverse biased and L and L_1 currents are flowing through.
Equating average voltage across inductor,
$DV_S = (1 - D) V_o$

$$\Rightarrow \quad \frac{V_o}{V_s} = \frac{D}{1 - D} \tag{1}$$

A. state space model of Zeta buck boost converter
Considering current is flowing through inductor I_L and voltage across capacitor V_C as a state variable. The average state space model for current fed zeta converter presented in Fig 1.3 is expressed as:

$$\begin{bmatrix} \frac{dV_{CS}}{dt} \\ \frac{dI_L}{dt} \\ \frac{dI_{L1}}{dt} \\ dV_C/dt \\ \frac{dV_{C1}}{dt} \end{bmatrix} = \begin{bmatrix} -\frac{1}{R_S C_s} & -\frac{1}{C_s} & 0 & 0 & \\ \frac{d}{L} & 0 & 0 & -\frac{(1-D)}{L} & 0 \\ 0 & 0 & 0 & \frac{D}{L_1} & -\frac{1}{L_1} \\ 0 & -(1-D)/C_1 & -D/C & 0 & 0 \\ 0 & -(1-D)/C_1 & -(1-D)/C_1 & 0 & \frac{-1}{R_L C_1} \end{bmatrix} \begin{bmatrix} V_{CS} \\ I_L \\ I_{L1} \\ V_C \\ V_{C1} \end{bmatrix} + \begin{bmatrix} \frac{1}{C_S} \\ 0 \\ 0 \\ 0 \\ \end{bmatrix} I_S$$

$V_S = V_{CS}$

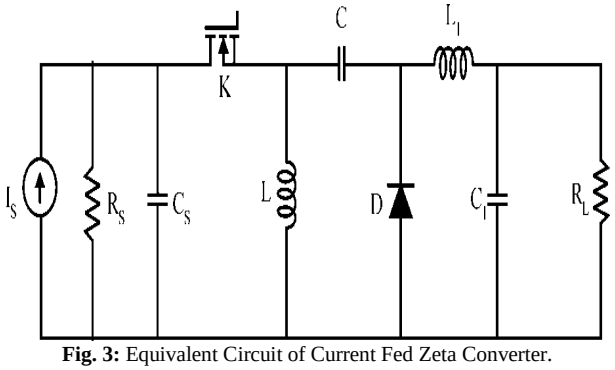


Fig. 3: Equivalent Circuit of Current Fed Zeta Converter.

Similarly, the small signal dynamic model is

$$\begin{bmatrix} \frac{d\tilde{V}_{CS}}{dt} \\ \frac{d\tilde{I}_L}{dt} \\ \frac{d\tilde{I}_{L1}}{dt} \\ \frac{d\tilde{V}_C}{dt} \\ \frac{d\tilde{V}_{C1}}{dt} \end{bmatrix} = \begin{bmatrix} -\frac{1}{R_S C_s} & -\frac{1}{C_s} & 0 & 0 & \\ \frac{\tilde{D}}{L} & 0 & 0 & -\frac{(1-\tilde{D})}{L} & 0 \\ 0 & 0 & 0 & \frac{\tilde{D}}{L_1} & -\frac{1}{L_1} \\ 0 & \frac{(1-\tilde{D})}{C} & -\frac{\tilde{D}}{C} & 0 & 0 \\ 0 & 0 & -\frac{1}{C_1} & 0 & \frac{-1}{R_L C_1} \end{bmatrix} \begin{bmatrix} \tilde{V}_{CS} \\ \tilde{I}_L \\ \tilde{I}_{L1} \\ \tilde{V}_C \\ \tilde{V}_{C1} \end{bmatrix} + \begin{bmatrix} 0 \\ \frac{\tilde{V}_C}{L} + \frac{\tilde{V}_{C1}}{L} \\ \frac{\tilde{V}_C}{L_1} + \frac{\tilde{V}_C}{L_1} \\ -\frac{1}{C_1}\tilde{I}_L - \frac{1}{C_1}\tilde{I}_{L_1} \\ 0 \end{bmatrix} \tilde{d}$$

$$\tilde{V}_S = \tilde{V}_{CS}$$

1.3. Proposed adaptive fuzzy logic controller based mppt

Fig 1.4 presents the overall block diagram portrayed of the FLC controller which consists of fuzzification, rule base and defuzzification as the major components. PV voltage and current are sensed which acts as a crisp value to the fuzzification block. The fuzzification converts crisp value to fuzzy value. Inference rule base have been employed to take the fuzzy decisions. These rules are based on IF/THEN basis which comprises mamdani max-min composition. The fuzzy parameters can be converted into numerical value using defuzzification method in which centroid method is employed. The FLC controller using MATLAB can be constructed and presented by Fig 1.5.

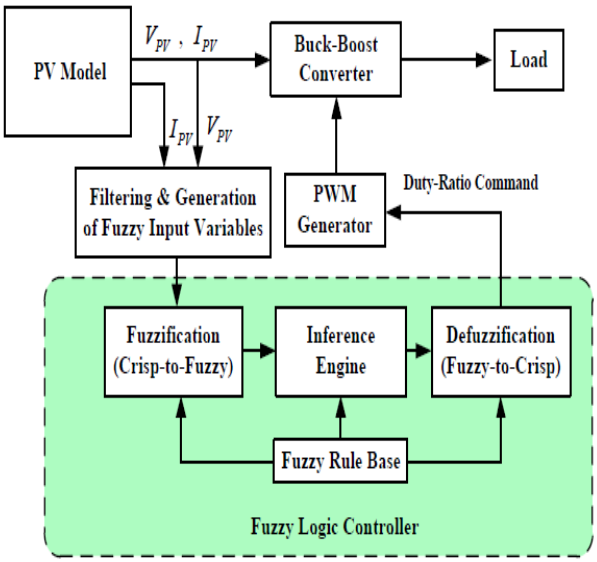


Fig. 4: FLC Structure.

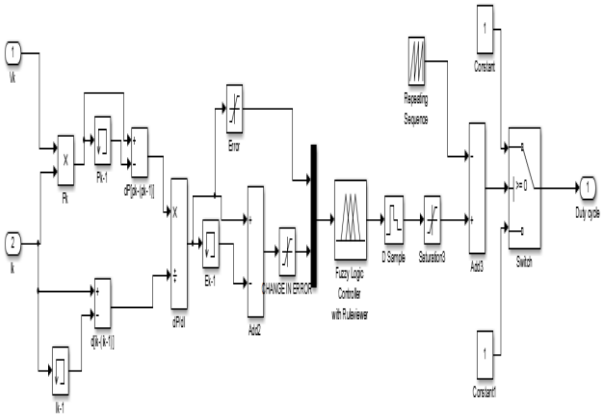


Fig.5: Flc Using Matlab/Simulink.

1.4. Hysteresis inverter control

In this paper hysteresis based non-linear inverter control has been employed for generation of switching pulses. In this method instantaneous reactive power has been used to produce inverter reference current. The 3-phase voltage and load currents have been transformed using clarke's transformation from a-b-c to α-β-0 frame. Using this transformation, reference current has been generated which acts as an input to hysteresis controller. Fig 1.6 depicts the hysteresis controller using SIMULINK. Generation of the reference current using MATLAB/SIMULINK is explained by Fig 1.7.

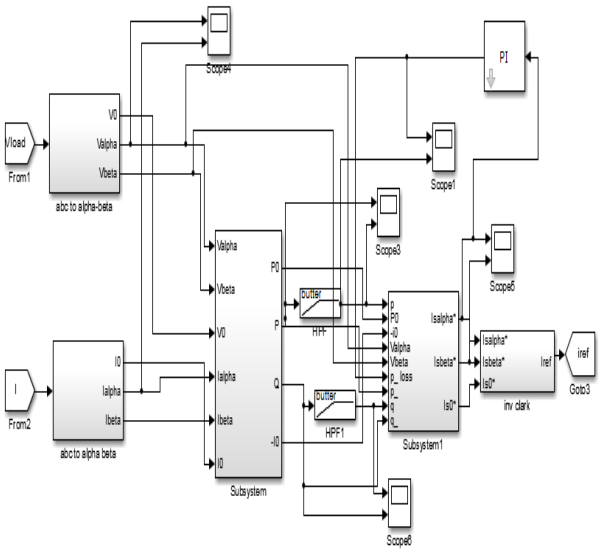


Fig. 6: Hysteresis Current Control Using Matlab/Simulink.

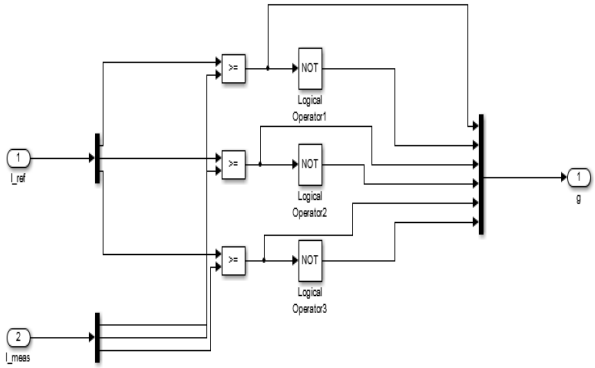


Fig.7: Generation of the Reference Current Using MATLAB/SIMULINK.

2. Response during system fault condition

Several faults have been created on grid side and total harmonic distortion (THD) has been analyzed. Fig 8 describes the MATLAB/Simulink based grid connected power system. The proposed system has been analyzed on 1000 W/m^2 irradiance with 25^0 C ambient temperature. Duration of fault is of 0.2 sec. The grid voltage, grid current, inverter current, active and reactive power with inverter current FFT have been studied. The proposed PV power system has been simulated under without and with fault conditions.

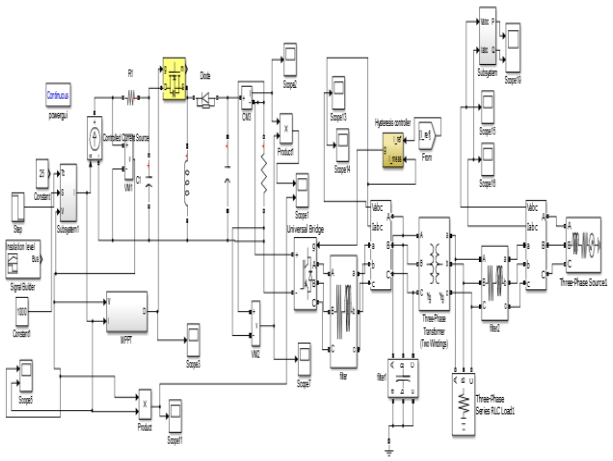
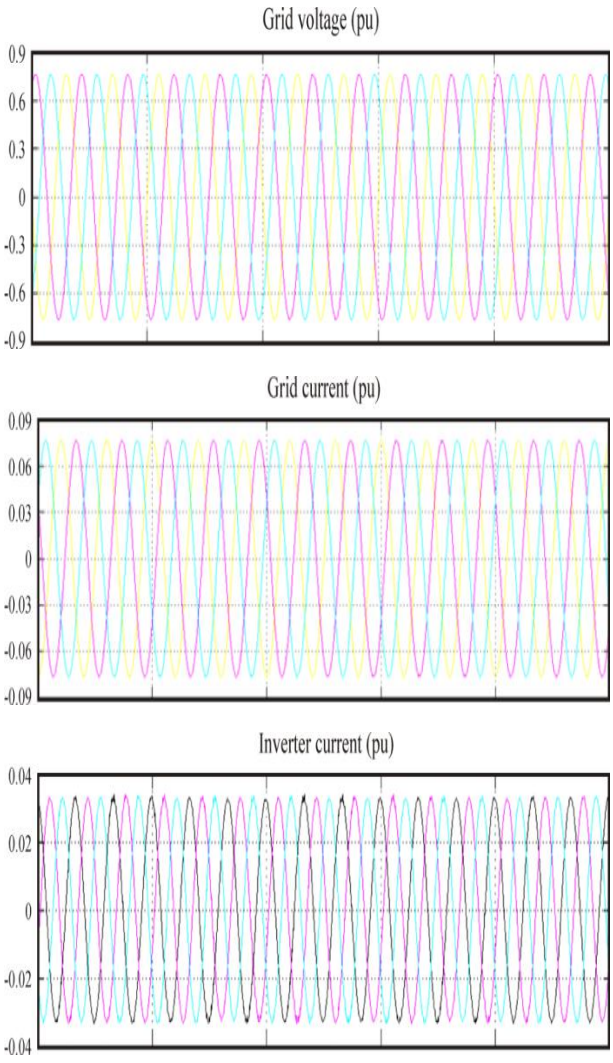


Fig.8: MATLAB/Simulink Based Grid Connected Power System.

The grid side fault can be generated for 0.2 sec from 0.4 to 0.6 seconds. The Fig. 9 shows various waveforms without Fault condition. The complete system behavior can be extensively analyzed under without fault and with fault conditions viz. three phase LG, LL, LLG, LLL and LLLG faulty conditions. The waveforms of grid voltage, grid current ,inverter current, FFT inverter current and grid side active and reactive power have been found at different fault conditions.



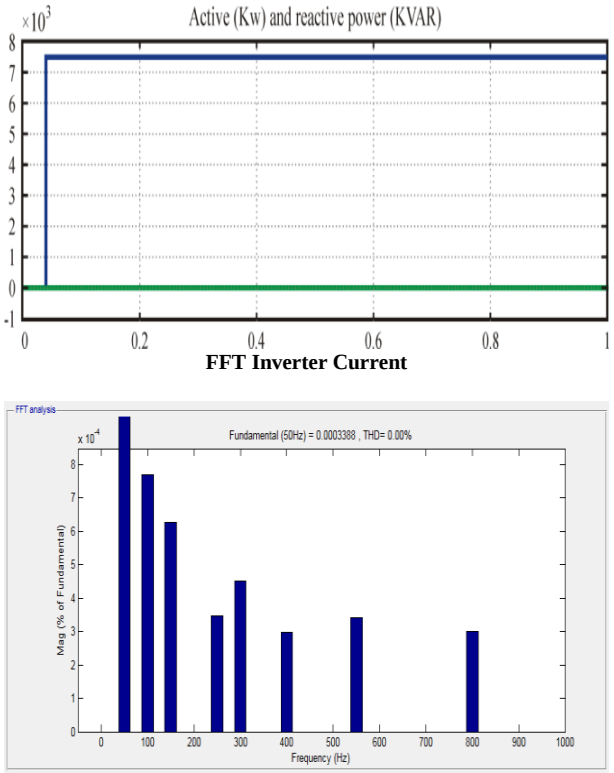


Fig. 9: Simulated Responses without Fault Condition. Grid Voltage, Grid Current, Inverter Current, Active and Reactive Power Grid Side, FFT Inverter Current.

The proposed system is studied without fault condition and the corresponding variation in grid voltage, inverter current and grid currents are shown in Fig. 1.9. From FFT analysis the inverter current has total harmonic distortion 0.00%. When a three phase LG fault occurs in the proposed system on grid side the corresponding variation in grid voltage, inverter current and grid currents are shown in Fig. 1.10. The fault is initiated at 0.4 sec, while cleared at 0.6 sec. In case of emergency condition a 3 phase circuit breaker is connected to the grid side to isolate the PV generation system from the distribution system. From FFT analysis the inverter current has total harmonic distortion 0.11%. When a three phase LL fault occurs in the proposed system on grid side the corresponding variation in grid voltage, inverter current and grid currents are shown in Fig 1.11. The fault is initiated at 0.4 sec, while cleared at 0.6 sec. Under LL fault, the amplitude of grid current is shown in simulated results. From FFT analysis the inverter current has total harmonic distortion 0.05%. When a three phase LLG fault occurs in the proposed system on grid side the corresponding variation in grid voltage, inverter current and grid currents are shown in Fig 1.12. The fault is initiated at 0.4 sec, while cleared at 0.6 sec. During line to ground fault period, the magnitude of grid current increases as shown in simulated results. From FFT analysis the inverter current has total harmonic distortion 0.10%. When a three phase LLL fault occurs in the proposed system on grid side the corresponding variation in grid voltage, inverter current and grid currents are shown in Fig. 1.13. The fault is initiated at 0.4 sec., while cleared at 0.6 sec. During line to ground fault period, the magnitude of grid current increases as shown in simulated results. From FFT analysis the inverter current has total harmonic distortion 0.03%. During three phase LLLG fault the magnitude of grid current increases as shown in Fig. 1.14. From FFT analysis the inverter current has total harmonic distortion 0.06%. When LLLG Fault occurs in the proposed system on grid side the corresponding variation in grid voltage, inverter current and grid currents are shown in simulated results. The fault is initiated at 0.4 sec., while cleared at 0.6 sec.

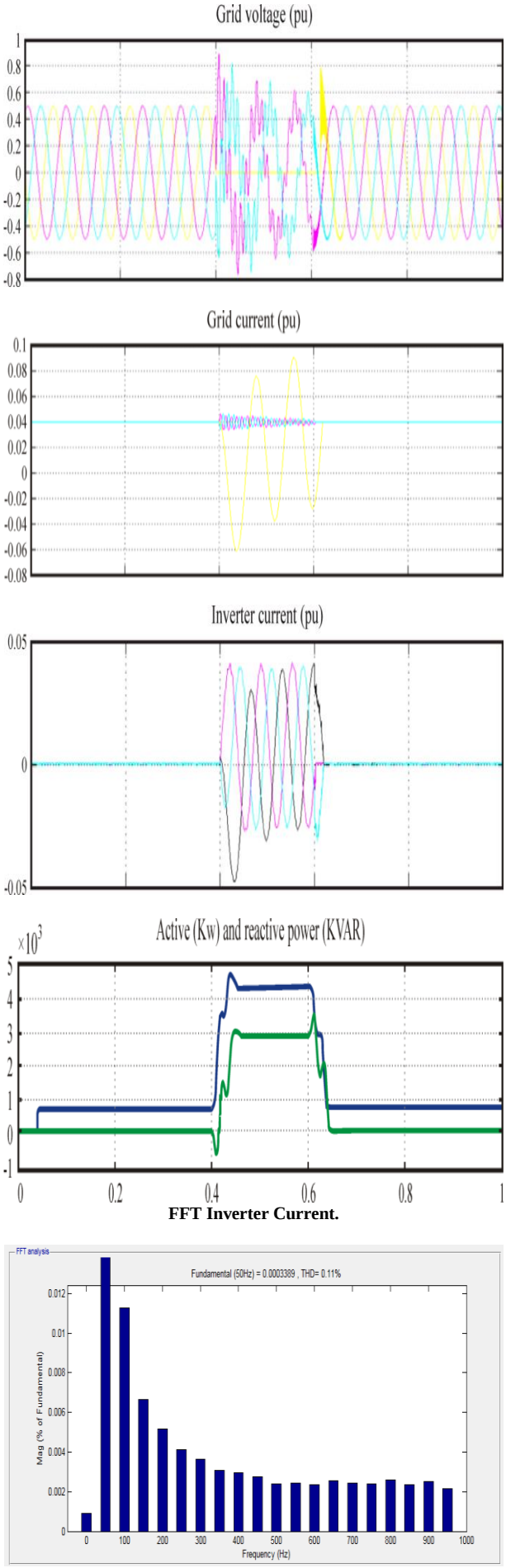
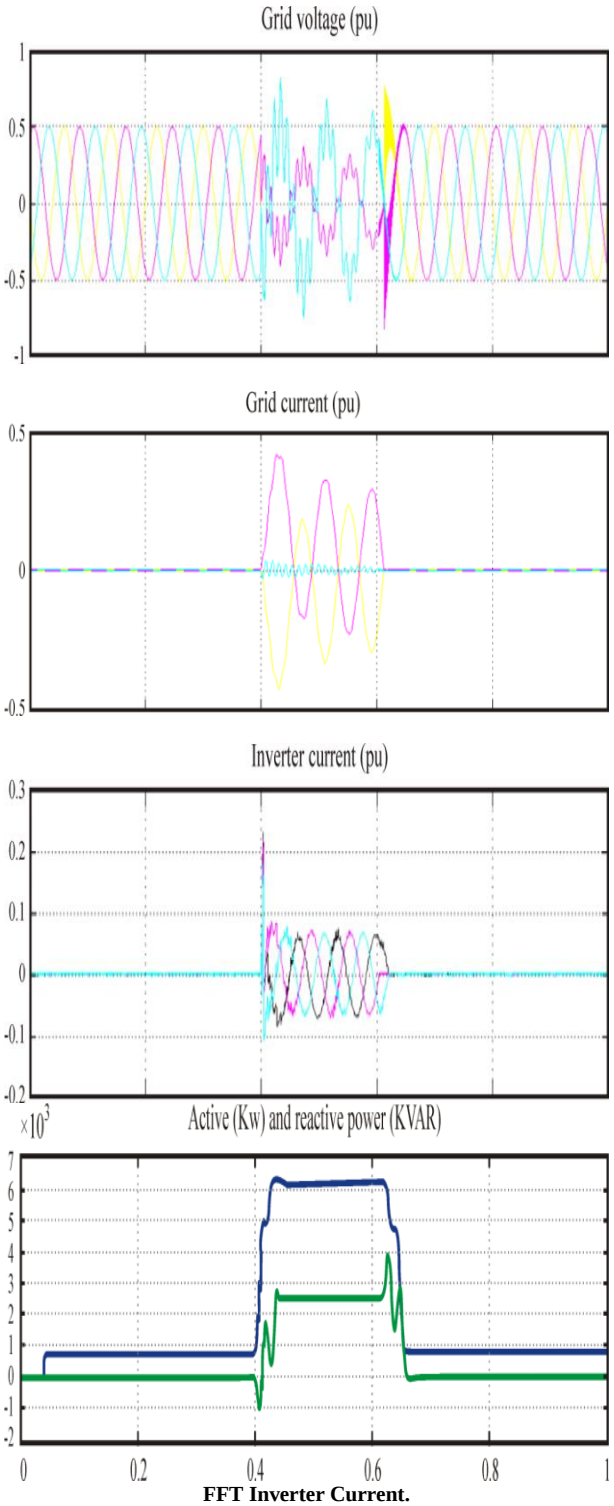


Fig. 10: Simulated Responses during LG Fault Condition. Grid Voltage, Grid Current, Inverter Current, Active and Reactive Power Grid Side, FFT Inverter Current.



FFT Inverter Current.

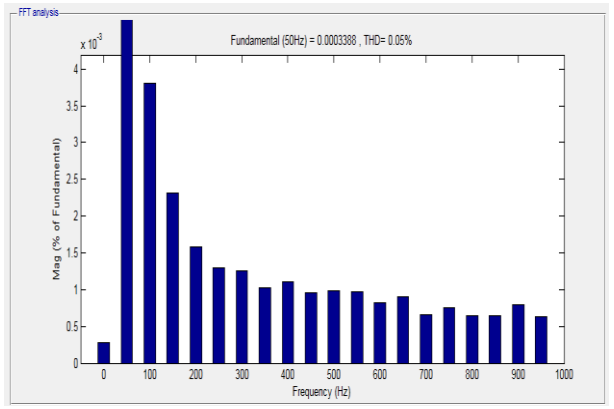
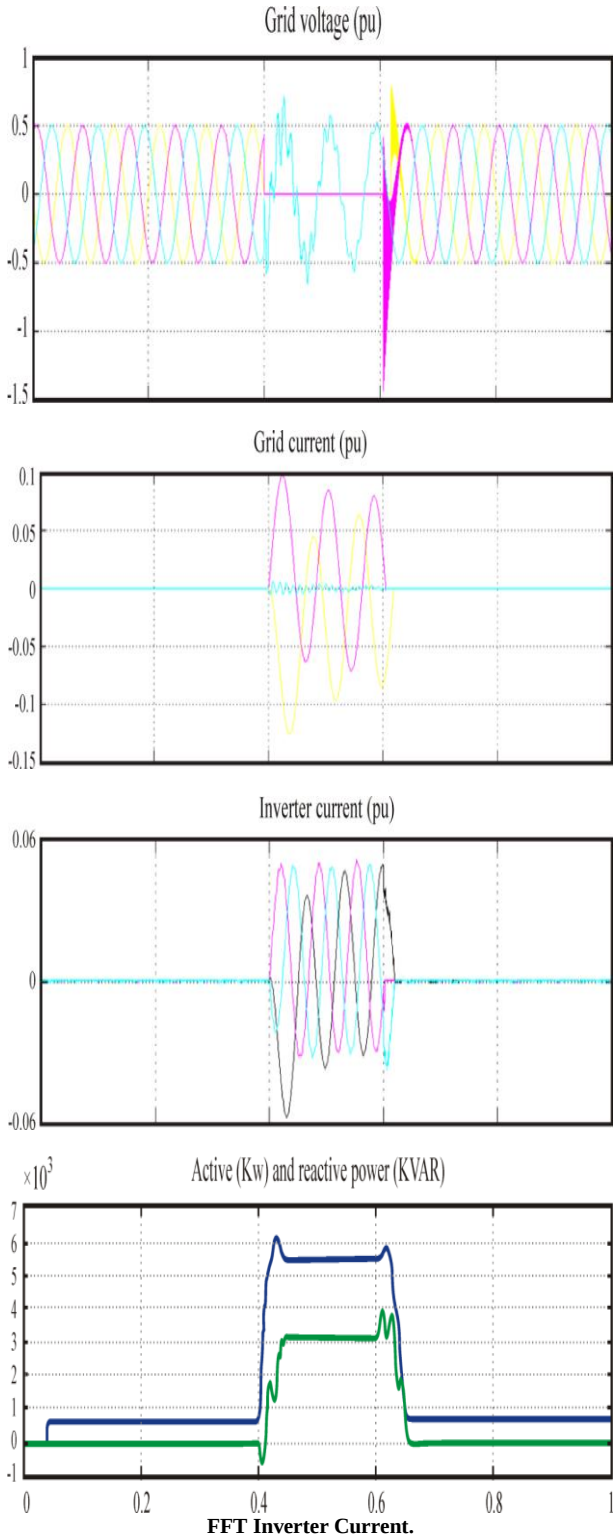


Fig. 11: Simulated Responses during LL Fault Condition. Grid Voltage, Grid Current, Inverter Current, Active and Reactive Power Grid Side, FFT Inverter Current



FFT Inverter Current.

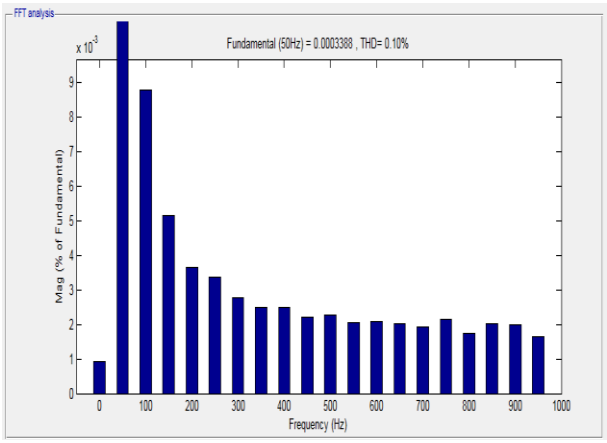
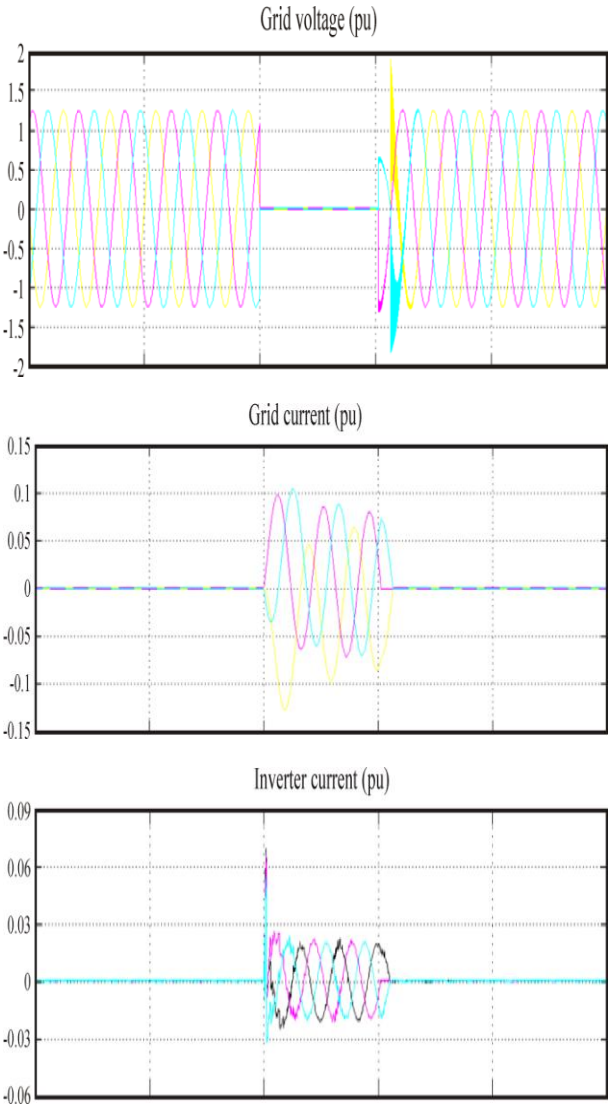
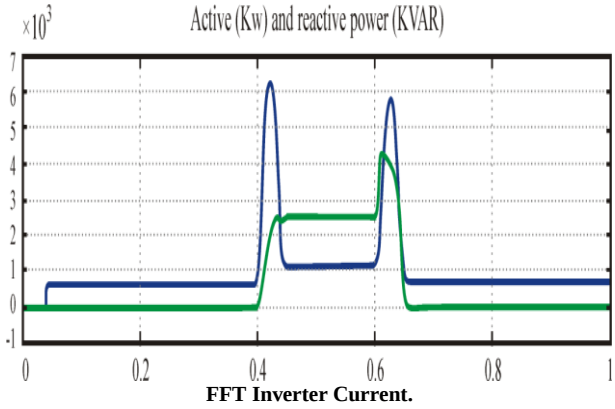


Fig. 12: Simulated Responses during LLG Fault Condition Grid Voltage, Grid Current, Inverter Current, Active and Reactive Power Grid Side, FFT Inverter Current





FFT Inverter Current.

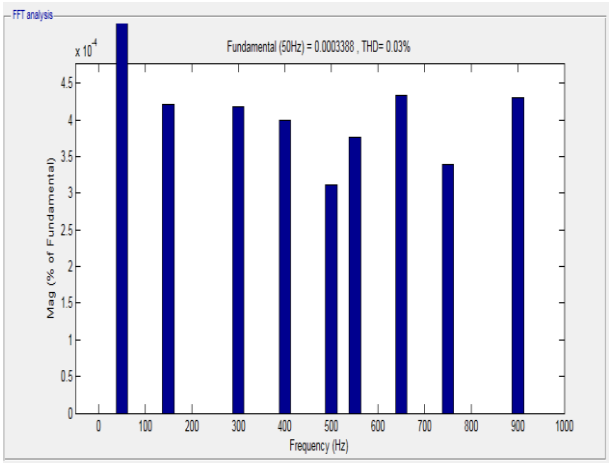
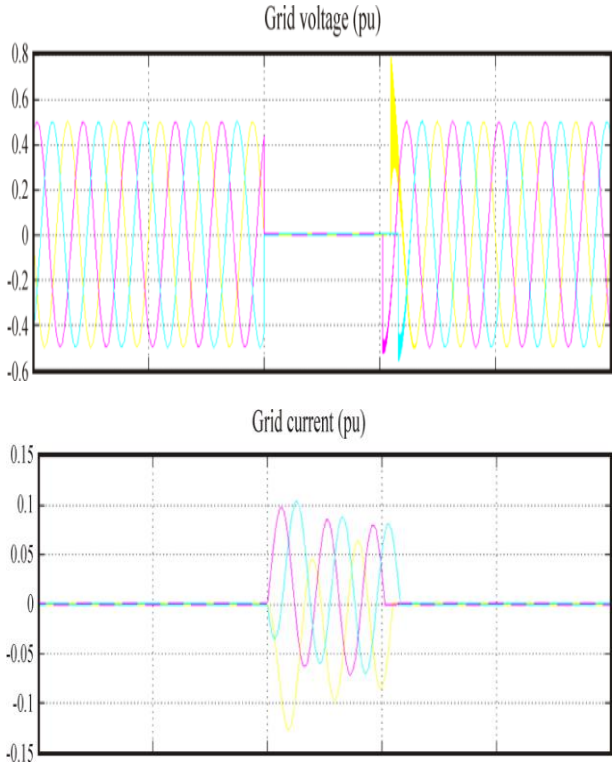


Fig. 13: Simulated Responses during LLL Fault Condition. Grid Voltage, Grid Current, Inverter Current, Active and Reactive Power Grid Side, FFT Inverter Current.



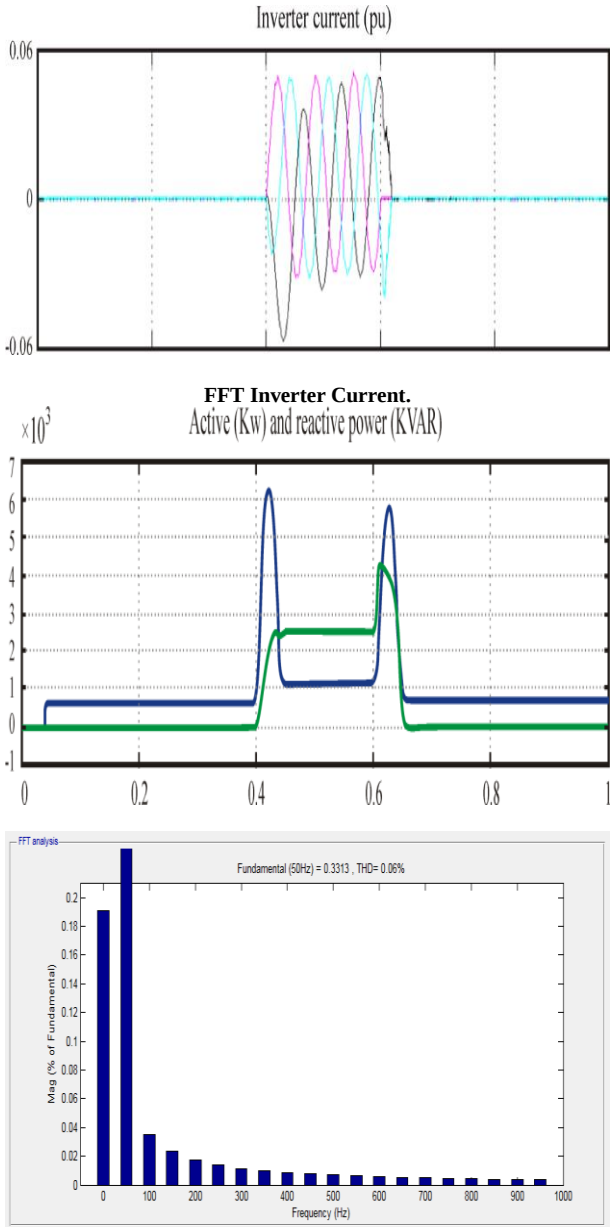


Fig. 14: Simulated Responses during LLLG Fault Condition. Grid Voltage, Grid Current, Inverter Current, Active and Reactive Power Grid Side, FFT Inverter Current

3. Power circuit structure

PV power circuit model for grid utility was built with protection circuitry and necessary control. Between PV module and three phase inverter, zeta converter works as an interface. The power circuit block diagram has been illustrated in Fig. 1.15.

4. Conclusion

This paper describes the various fault analysis for grid PV power system. Simulated results explain that within 0.2 sec, the proposed power system gains stability at normal frequency. The different fault on grid side has been analyzed for proper design of protection circuit. The MPPT and inverter control strategies for PV power system are working efficiently.

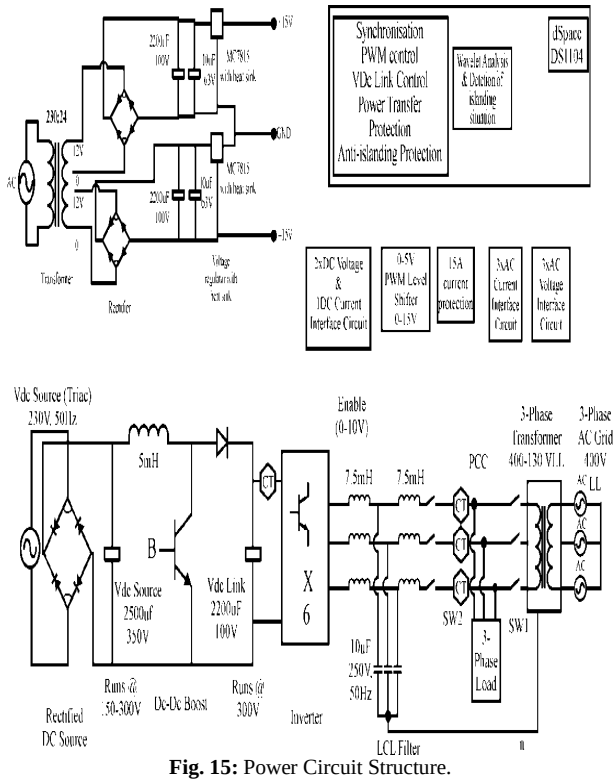


Fig. 15: Power Circuit Structure.

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